# Congruence Closure with Free Variables

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**Abstract.** Many verification techniques nowadays successfully rely on SMT solvers as back-ends to automatically discharge proof obligations. These solvers generally rely on various instantiation techniques to handle quantifiers. We here show that the major instantiation techniques in SMT solving can be cast in a unifying framework for handling quantified formulas with equality and uninterpreted functions. This framework is based on the problem of $E$-ground (dis)unification, a variation of the classic rigid $E$-unification problem. We introduce a sound and complete calculus to solve this problem in practice: Congruence Closure with Free Variables (CCFV). Experimental evaluations of implementations of CCFV in the state-of-the-art solver CVC4 and in the solver veriT exhibit improvements in the former and makes the latter competitive with state-of-the-art solvers in several benchmark libraries stemming from verification efforts.

## 1 Introduction

SMT solvers [8] are highly efficient at handling large ground formulas with interpreted symbols, but they still struggle with quantified formulas. Pure quantified first-order logic is best handled with *resolution* and *superposition*-based theorem proving [3]. Although there are first attempts to unify such techniques with SMT [13], the main approach used in SMT is still *instantiation*: quantified formulas are reduced to ground ones and refuted with the help of decision procedures for ground formulas. The main instantiation techniques are $E$-matching based on triggers [12,17,26], finding conflicting instances [24] and model-based quantifier instantiation (MBQI) [19,25]. Each of these techniques contributes to the efficiency of state-of-the-art solvers, yet each one is typically implemented independently.

We introduce the $E$-ground (dis)unification problem as the cornerstone of a unique framework in which all these techniques can be cast. This problem relates

to the classic problem of rigid $E$-unification and is also NP-complete. Solving $E$-ground (dis)unification amounts to finding substitutions such that literals containing free variables hold in the context of currently asserted ground literals. Since the instantiation domain of those variables can be bound, a possible way of solving the problem is by first non-deterministically guessing a substitution and checking if it is a solution. The *Congruence Closure with Free Variables* algorithm (CCFV, for short) presented here is a practical decision procedure for this problem based on the classic congruence closure algorithm [21,22]. It is goal-oriented: solutions are constructed incrementally, taking into account the congruence closure of the terms defined by the equalities in the context and the possible assignments to the variables.

We then show how to build on CCFV to implement trigger-based, conflict-based and model-based instantiation. An experimental evaluation of the technique is presented, where our implementations exhibits improvements over state-of-the-art approaches.

## 1.1   Related Work

Instantiation techniques for SMT have been studied extensively. Heuristic instantiation based on $E$-matching of selected triggers was introduced by Detlefs et al. [17]. A highly efficient implementation of $E$-matching was presented by de Moura and Bjørner [12]; it relies on elaborated indexing techniques and generation of machine code for optimizing performance. Rümmer uses triggers alongside a classic tableaux method [26]. Trigger based instantiation unfortunately produces many irrelevant instances. To tackle this issue, a goal-oriented instantiation technique producing only useful instances was introduced by Reynolds et al. [24]. CCFV shares resemblance with this algorithm, the search being based on the structure of terms and a current model coming from the ground solver. The approach here is however more powerful and more general, and somehow subsumes this previous technique. Ge and de Moura's model based quantifier instantiation (MBQI) [19] provides a complete method for first-order logic through successive derivation of conflicting instances to refine a candidate model for the whole formula, including quantifiers. Thus it also allows the solver to find finite models when they exist. Model checking is performed with a separate copy of the ground SMT solver searching for a conflicting instance. Alternative methods for model construction and checking were presented by Reynolds et al. [25]. Both these model based approaches [19,25] allow integration of theories beyond equality, while CCFV for now only handles equality and uninterpreted functions.

Backeman and Rümmer solve the related problem of rigid $E$-unification through encoding into SAT, using an off-the-shelf SAT solver to compute solutions [5]. Our work is more in line with goal-oriented techniques as those by Goubault [20] and Tiwari et al. [27]; congruence closure algorithms being very efficient at checking solutions, we believe they can also be the core of efficient algorithms to discover them. CCFV differs from those previous techniques notably, since it handles disequalities and since the search for solutions is pruned based on the structure of a ground model and is thus most suitable for an SMT context.

## 2    Notations and Basic Definitions

We refer to classic notions of many-sorted first-order logic (e.g. by Baader and Nipkow [1] and by Fitting [18]) as the basis for notations in this paper. Only the most relevant are mentioned.

A *first-order language* is a tuple $\mathscr{L} = \langle \mathcal{S}, \mathcal{X}, \mathcal{P}, \mathcal{F}, sort \rangle$ in which $\mathcal{S}, \mathcal{X}, \mathcal{P}$ and $\mathcal{F}$ are disjoint enumerable sets of *sort*, *variable*, *predicate* and *function symbols*, respectively, and $sort : \mathcal{X} \cup \mathcal{F} \cup \mathcal{P} \to \mathcal{S}^+$ is a function assigning sorts, according to the symbols' arities. Nullary functions and predicates are called *constants* and *propositions*, respectively. *Formulas* and *terms* are generated in a well-sorted manner by

$$t ::= x \mid f(t, \ldots, t) \qquad \varphi ::= t \simeq t \mid p(t, \ldots, t) \mid \neg\varphi \mid \varphi \vee \varphi \mid \forall x_1 \ldots x_n.\varphi$$

in which $x, x_1, \ldots, x_n \in \mathcal{X}$, $p \in \mathcal{P}$ and $f \in \mathcal{F}$. The predicate symbol $\simeq$ stands for *equality*. The terms in a formula $\varphi$ are denoted by $\mathbf{T}(\varphi)$. In a function or predicate application, the symbol being applied is referred as the term's *top symbol*. The *free variables* of a formula $\varphi$ are denoted by $\mathrm{FV}(\varphi)$. A formula or term is *ground* iff it contains no variables. Whenever convenient, an enumeration of symbols $s_1, \ldots, s_n$ will be represented as $\mathbf{s}$.

A *substitution* $\sigma$ is a mapping from variables to terms. The application of $\sigma$ to the formula $\varphi$ (respectively the term $t$) is denoted by $\varphi\sigma$ ($t\sigma$). The *domain of* $\sigma$ is the set $dom(\sigma) = \{x \mid x \in \mathcal{X}$ and $x\sigma \neq x\}$, while the *range of* $\sigma$ is $ran(\sigma) = \{x\sigma \mid x \in dom(\sigma)\}$. A substitution $\sigma$ is *ground* iff every term in $ran(\sigma)$ is ground and *acyclic* iff, for any variable $x$, $x$ does not occur in $x\sigma \ldots \sigma$. For an acyclic substitution, $\sigma^\star$ is the fixed point substitution of $\sigma$.

Given a set of ground terms $\mathbf{T}$ closed under the subterm relation and a congruence relation $\simeq$ on $\mathbf{T}$, a *congruence* over $\mathbf{T}$ is a subset of $\{s \simeq t \mid s, t \in \mathbf{T}\}$ closed under entailment. The *congruence closure* (CC, for short) of a set of equations $E$ on a set of terms $\mathbf{T}$ is the least congruence on $\mathbf{T}$ containing $E$. Given a consistent set of equality literals $E$, two terms $t_1, t_2$ are said *congruent* iff $E \models t_1 \simeq t_2$ and *disequal* iff $E \models t_1 \not\simeq t_2$. The *congruence class* in $\mathbf{T}$ of a given term is the set of terms in $\mathbf{T}$ congruent to it. The signature of a term is the term itself for a nullary symbol, and $f(c_1, \ldots c_n)$ for a term $f(t_1, \ldots t_n)$ with $c_i$ being the class of $t_i$. The *signature class* of $t$ is a set $[t]_E$ containing one and only one term in the class of $t$ for each signature. Notice that the signature class of two terms in the same class is the same set of terms, and is a subset of the congruence class. We drop the subscript in $[t]_E$ when $E$ is clear from the context. The *set of signature classes* of $E$ on a set of terms $\mathbf{T}$ is $E^{\mathrm{CC}} = \{[t] \mid t \in \mathbf{T}\}$.

## 3    *E*-ground (Dis)unification

For simplicity, and without loss of generality, we consider formulas in Skolem form, with all quantified subformulas being quantified clauses; we also assume all atomic formulas are equalities. SMT solvers proceed by enumerating the models for the propositional abstraction of the input formula, i.e. the formula

obtained by replacing every atom and quantified subformula by a proposition. Such a model of the propositional abstraction corresponds to a set $E \cup \mathcal{Q}$, in which $E$ and $\mathcal{Q}$ are conjunctive sets of ground literals and quantified formulas, respectively. If $E \cup \mathcal{Q}$ is consistent, all of its models also satisfy the input formula; if not, a new candidate model is derived. The ground SMT solver first checks the satisfiability of $E$, and, if it is satisfiable, proceeds to reason on the set of quantified formulas $\mathcal{Q}$. Ground instances $\mathcal{I}$ are derived from $\mathcal{Q}$, and subsequently the satisfiability of $E \cup \mathcal{I}$ is checked. This is repeated until either a conflict is found, and a new model for the propositional abstraction must be produced, or no more instantiations are possible. Of course, the whole process might not terminate and the solver might loop indefinitely.

In this approach, a central problem is to determine which instances $\mathcal{I}$ to derive. Section 5 shows that the problem of finding instances via existing instantiation techniques can be reduced to the problem of E-ground (dis)unification.

**Definition 1 (*E*-ground (dis)unification).** *Given two finite sets of equality literals $E$ and $L$, $E$ being ground, the E-ground (dis)unification problem is that of finding substitutions $\sigma$ such that $E \models L\sigma$.*

$E$-ground (dis)unification can be recast as the classic problem of (non-simultaneous) rigid $E$-unification (transformation proof in Appendix B of [6]), i.e. computing substitutions $\sigma$ such that $E^{eq}\sigma \models s\sigma \simeq t\sigma$, in which $E^{eq}$ is a set of equations and $s, t$ are terms. Rigid $E$-unification has been studied extensively in the context of automated theorem proving [2,10,15]. In particular, its intrinsic relation with congruence closure has been investigated by Goubault [20] and Tiwari et al. [27], in which variations of the classic procedure are integrated with first-order rewriting techniques and the search for solutions is guided by the structure of the terms. We build on these ideas to develop our method for solving $E$-ground (dis)unification, as discussed in Sect. 4.

*Example 1.* Consider the sets $E = \{f(a) \simeq f(b), h(a) \simeq h(c), g(b) \not\simeq h(c)\}$ and $L = \{h(x_1) \simeq h(c), h(x_2) \not\simeq g(x_3), f(x_1) \simeq f(x_3), x_4 \simeq g(x_5)\}$. A solution for their $E$-ground (dis)unification problem is $\{x_1 \mapsto a, x_2 \mapsto c, x_3 \mapsto b, x_4 \mapsto g(x_5)\}$.

The above example shows that $x_5$ can be mapped to any term; this $E$-ground (dis)unification problem has infinitely many solutions. However, here, like in general,[1] the set of all solutions can be finitely represented:

**Theorem 1.** *Given an E-ground (dis)unification problem, if a substitution $\sigma$ exists such that $E \models L\sigma$, then there is an acyclic substitution $\sigma'$ such that $ran(\sigma') \subseteq \mathbf{T}(E \cup L)$, $\sigma'^{\star}$ is ground, and $E \models L\sigma'^{\star}$.*

*Proof.* The proof can be found in Appendix A of [6]. □

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[1] It is assumed, without loss of generality, that $\mathbf{T}(E \cup L)$ contains at least one ground term of each sort in $E \cup L$.

As a corollary, the problem is in NP: it suffices indeed to guess an acyclic substitution with $ran(\sigma') \subseteq \mathbf{T}(E \cup L)$, and check (polynomially) that it is a solution. The problem is also NP-hard, by reduction of 3-SAT (Appendix C of [6]). As our experiments show, however, a concrete algorithm effective in practice is possible.

## 4    Congruence Closure with Free Variables

In this section we describe a calculus to find each substitution $\sigma$ solving an $E$-ground (dis)unification problem $E \models L\sigma$. This calculus, *Congruence Closure with Free Variables* (CCFV), uses a congruence closure algorithm as a core element to guide the search and build solutions. It proceeds by building a set of equations $E_\sigma$ such that $E \cup E_\sigma \models L$, in which $E_\sigma$ corresponds to a solution substitution, built step by step, by decomposing $L$ in a top-down manner into sets of simpler constraints.

*Example 2.* Considering again $E$ and $L$ as in Example 1, the calculus should find $\sigma$ such that

$$f(a) \simeq f(b), h(a) \simeq h(c), g(b) \not\simeq h(c)$$
$$\models (h(x_1) \simeq h(c) \land h(x_2) \not\simeq g(x_3) \land f(x_1) \simeq f(x_3) \land x_4 \simeq g(x_5)) \, \sigma$$

For $L$ to be entailed by $E \cup E_\sigma$, each of its literals contributes to equations in $E_\sigma$ in the following manner:

- $h(x_1) \simeq h(c)$: either $x_1 \simeq c$ or $x_1 \simeq a$ belongs to $E_\sigma$;
- $h(x_2) \not\simeq g(x_3)$: either $x_2 \simeq c \land x_3 \simeq b$ or $x_2 \simeq a \land x_3 \simeq b$ belongs to $E_\sigma$;
- $f(x_1) \simeq f(x_3)$: either $x_1 \simeq x_3$ or $x_1 \simeq a \land x_3 \simeq b$ or $x_1 \simeq b \land x_3 \simeq a$ must be in $E_\sigma$;
- $x_4 \simeq g(x_5)$: the literal itself must be in $E_\sigma$.

One solution is thus $E_\sigma = \{x_1 \simeq a, x_2 \simeq a, x_3 \simeq b, x_4 \simeq g(x_5)\}$, corresponding to the acyclic substitution $\sigma = \{x_1 \mapsto a, x_2 \mapsto a, x_3 \mapsto b, x_4 \mapsto g(x_5)\}$. Notice that, for any ground term $t \in \mathbf{T}(E \cup L)$, $\sigma_g = \sigma \cup \{x_5 \mapsto t\}$ is such that $ran(\sigma_g) \subseteq \mathbf{T}(E \cup L)$, $\sigma_g^\star$ is ground, and $E \models L\sigma_g^\star$.

### 4.1    The Calculus

Given an $E$-ground (dis)unification problem $E \models L\sigma$, the CCFV calculus computes the various possible $E_\sigma$ corresponding to a coverage of all substitution solutions, i.e. such that $E \cup E_\sigma \models L$. We describe the calculus as a set of rules that operate on states of the form $E_\sigma \Vdash_E C$, in which $C$ is a (disjunctive normal form) formula stemming from the decomposition of $L$ into simpler constraints, and $E_\sigma$ is a conjunctive set of equalities representing a partial solution. Starting from the initial state $\varnothing \Vdash_E L$, the right side of the state is progressively decomposed, whereas the left side is step by step augmented with new equalities building the candidate solution. Example 2 shows that, for a literal to be entailed by $E \cup E_\sigma$,

**Table 1.** The CCFV calculus in equational FOL. $E$ is fixed from a problem $E \models L\sigma$.

$$\frac{E_\sigma \Vdash_E x \simeq s \wedge C}{E_\sigma \cup \{x \simeq s\} \Vdash_E rep(C)} \ \textsc{Assign} \qquad \text{if } x \notin \text{FV}(s)$$

$$\frac{E_\sigma \Vdash_E x \simeq f(u_1, \ldots, u_n) \wedge C}{E_\sigma \Vdash_E \bigvee_{[t]\in E^{cc}, f(t_1,\ldots,t_n)\in[t]}(x \simeq t \wedge u_1 \simeq t_1 \wedge \cdots \wedge u_n \simeq t_n \wedge C)} \ \textsc{U\_var}$$
$$\text{if } x \in \text{FV}(f(u_1, \ldots, u_n))$$

$$\frac{E_\sigma \Vdash_E f(u_1, \ldots, u_n) \simeq f(s_1, \ldots, s_n) \wedge C}{\begin{array}{c} E_\sigma \Vdash_E (u_1 \simeq s_1 \wedge \cdots \wedge u_n \simeq s_n \wedge C) \vee \\ \bigvee_{\substack{[t]\in E^{cc}, \\ f(t_1,\ldots,t_n)\in[t], f(t_1',\ldots,t_n')\in[t]}} \left( \begin{array}{c} u_1 \simeq t_1 \wedge \cdots \wedge u_n \simeq t_n \wedge \\ s_1 \simeq t_1' \wedge \cdots \wedge s_n \simeq t_n' \wedge C \end{array} \right) \end{array}} \ \textsc{U\_comp}$$

$$\frac{E_\sigma \Vdash_E f(u_1, \ldots, u_n) \simeq g(s_1, \ldots, s_m) \wedge C}{E_\sigma \Vdash_E \bigvee_{\substack{[t]\in E^{cc}, \\ f(t_1,\ldots,t_n)\in[t], g(t_1',\ldots,t_m')\in[t]}} \left( \begin{array}{c} u_1 \simeq t_1 \wedge \cdots \wedge u_n \simeq t_n \wedge \\ s_1 \simeq t_1' \wedge \cdots \wedge s_m \simeq t_m' \wedge C \end{array} \right)} \ \textsc{U\_gen}$$
$$\text{if } f \neq g$$

$$\frac{E_\sigma \Vdash_E x \not\simeq y \wedge C}{E_\sigma \Vdash_E \bigvee_{[t_1],[t_2]\in E^{cc}, E\models t_1 \not\simeq t_2}(x \simeq t_1 \wedge y \simeq t_2 \wedge C)} \ \textsc{R\_var}$$

$$\frac{E_\sigma \Vdash_E x \not\simeq f(s_1, \ldots, s_n) \wedge C}{E_\sigma \Vdash_E \bigvee_{\substack{[t],[t']\in E^{cc}, \\ E\models t\not\simeq t', f(t_1',\ldots,t_n')\in[t']}}(x \simeq t \wedge s_1 \simeq t_1' \wedge \cdots \wedge s_n \simeq t_n' \wedge C)} \ \textsc{R\_fapp}$$

$$\frac{E_\sigma \Vdash_E f(u_1, \ldots, u_n) \not\simeq g(s_1, \ldots, s_m) \wedge C}{E_\sigma \Vdash_E \bigvee_{\substack{[t],[t']\in E^{cc}, E\models t\not\simeq t', \\ f(t_1,\ldots,t_n)\in[t], g(t_1',\ldots,t_m')\in[t']}} \left( \begin{array}{c} u_1 \simeq t_1 \wedge \cdots \wedge u_n \simeq t_n \wedge \\ s_1 \simeq t_1' \wedge \cdots \wedge s_m \simeq t_m' \wedge C \end{array} \right)} \ \textsc{R\_gen}$$

$$\frac{E_\sigma \Vdash_E C_1 \vee C_2}{E_\sigma \Vdash_E C_1 \qquad E_\sigma \Vdash_E C_2} \ \textsc{Split} \qquad \frac{E_\sigma \Vdash_E C}{E_\sigma \Vdash_E \top} \ \textsc{Yield} \quad \text{if } E \cup E_\sigma \models C$$

$$\frac{E_\sigma \Vdash_E C}{E_\sigma \Vdash_E \bot} \ \textsc{Fail} \quad \begin{array}{l} \text{if no other rule can be applied; or} \\ C \text{ is a conjunction and } E \not\models \ell, \text{ for some ground } \ell \in C \end{array}$$

sometimes several solutions $E_\sigma$ exist, thus the calculus involves branching. To simplify the presentation, the rules do not apply branching directly, but build disjunctions on the right part of the state, those disjunctions later leading to branching. A branch is closed when its constraint is decomposed into either $\bot$ or $\top$. The latter are branches for which $E \cup E_\sigma \models L$ holds.

The set of CCFV derivation rules is presented in Table 1; $t$ stands for a ground term, $x, y$ for variables, $u$ for non-ground terms, $u_1, \ldots, u_n$ for terms

such that at least one is non-ground and $s, s_1, \ldots, s_n$ for terms in general. Rules are applied top-down, the symmetry of equality being used implicitly. Each rule simplifies the constraint of the right hand side of the state, and as a consequence any derivation strategy is terminating (Theorem 2).

When an equality is added to the left hand side of a state $E_\sigma \Vdash_E C$ (rule ASSIGN), the constraint $C$ is normalized with respect to congruence closure to reflect the assignments to variables. That is, all terms in $C$ are representatives of classes in the congruence closure of $E \cup E_\sigma$. We write

$$rep(x) = \begin{cases} \text{some chosen } y \in [x]_{E_\sigma} & \text{if all terms in } [x]_{E_\sigma} \text{ are variables} \\ rep(f(\mathbf{s})) & \text{otherwise, for some } f(\mathbf{s}) \in [x]_{E_\sigma} \end{cases}$$

$$rep(f(s_1, \ldots, s_n)) = \begin{cases} f(s_1, \ldots, s_n) & \text{if } f(s_1, \ldots, s_n) \text{ is ground} \\ f(rep(s_1), \ldots, rep(s_n)) & \text{otherwise} \end{cases}$$

and write $rep(C)$ to denote the result of applying $rep$ on both sides of each literal $s \simeq s'$ or $s \not\simeq s'$ in $C$. The above definition of $rep$ leaves room for some choice of representative, but soundness and completeness are not impacted by the choice. What actually matters is whether the representative is a variable, a ground term or a non-ground function application. The ASSIGN rule adds equations from the right side of the state into the tentative solution in the left side of the state: it extends $E_\sigma$ with the mapping for a variable. Because $C$ is replaced by $rep(C)$, one variable (either $x$, or $s$ if it is a variable) disappears from the right side.

The other rules can be divided into two categories. First are the branching rules (U_VAR through R_GEN), which enumerate all possibilities for deriving the entailment of some literal from $C$. For example, the rule U_COMP enumerates the possibilities for which a literal of the form $f(u_1, \ldots, u_n) \simeq f(s_1, \ldots, s_n)$ is entailed, which may be either due to syntactic unification, since both terms have the same top symbol, or by matching $f$-terms occurring in the same signature class of $E^{\mathrm{CC}}$. Second are the structural rules (SPLIT, FAIL and YIELD), which create or close branches. SPLIT creates branches when there are disjunctions in the constraint. FAIL closes a branch when it is no longer possible to build on the current solution to entail the remaining constraints. YIELD closes a branch when all remaining constraints are already entailed by $E \cup E_\sigma$, with $E_\sigma$ embodying a solution for the given $E$-ground (dis)unification problem. Theorems 3 and 4 state the correctness of the calculus.

If a branch is closed with YIELD, the respective $E_\sigma$ defines a substitution $\sigma = \{x \mapsto rep(x) \mid x \in \mathrm{FV}(L)\}$. The set $\mathrm{SOLS}(E_\sigma)$ of all ground solutions extractable from $E_\sigma$ is composed of substitutions $\sigma_g$ which extend $\sigma$ by mapping all variables in $ran(\sigma^\star)$ into ground terms in $\mathbf{T}(E \cup L)$, s.t. each $\sigma_g$ is acyclic, $\sigma_g^\star$ ground and $E \models L\sigma_g^\star$.

## 4.2   A Strategy for the Calculus

A possible derivation strategy for CCFV, given an initial state $\varnothing \Vdash_E L$, is to apply the sequence of steps described below at each state $E_\sigma \Vdash_E C$. Let SEL be a function that selects a literal from a conjunction according to some heuristic,

such as selecting first literals with less variables or literals whose top symbols have less ground signatures in $E^{\mathrm{cc}}$. The result of SEL is denoted *selected literal*. Since no two rules can be applied on the same literal, the function SEL effectively enforces an order on the application of the rules.

1. *Select branch*: While $C$ is a disjunction, apply SPLIT and consider the leftmost branch, by convention.
2. *Simplify constraint*: Apply the rule for which $\mathrm{SEL}(C)$ is amenable.
3. *Discard failure*: If FAIL was applied or a branching rule had the empty disjunction as a result, discard this branch and consider the next open branch.
4. *Mark success*: If all remaining constraints in the branch are entailed by $E \cup E_\sigma$, apply YIELD to mark the successful branch and then consider the next open branch.

A solution $\sigma$ for the $E$-ground (dis)unification problem $E \models L\sigma$ can be extracted at each branch terminated by the YIELD rule (Corollary 1).

*Example 3.* Consider again $E$ and $L$ as in Example 1. The set of signature classes of $E$ is

$$E^{\mathrm{cc}} = \{[a], [b], [c], [f(a), f(b)], [h(a), h(c)], [g(b)]\}$$

Let SEL select the literal in $C$ with the minimum number of variables. The derivation tree produced by CCFV for this problem is shown below. Selected literals are underlined. Disjunctions and the application of SPLIT are kept implicit to simplify the presentation, as is the handling of $x_4 \simeq g(x_5)$. Its entailment does not relate with the other literals in $L$ and it can be handled by an early application of ASSIGN.

$$\frac{\varnothing \Vdash_E \underline{h(x_1) \simeq h(c)}, h(x_2) \not\simeq g(x_3), f(x_1) \simeq f(x_3)}{\mathcal{A} \qquad\qquad\qquad\qquad \mathcal{B}} \text{ U\_COMP}$$

with $\mathcal{A}$ being

$$\frac{\dfrac{\dfrac{\dfrac{\dfrac{\varnothing \Vdash_E \underline{x_1 \simeq c}, h(x_2) \not\simeq g(x_3),\ f(x_1) \simeq f(x_3)}{\{x_1 \simeq c\} \Vdash_E h(x_2) \not\simeq g(x_3), \underline{f(c) \simeq f(x_3)}} \text{ ASSIGN}}{\{x_1 \simeq c\} \Vdash_E h(x_2) \not\simeq g(x_3), \underline{x_3 \simeq c}} \text{ U\_COMP}}{\{x_1 \simeq c, x_3 \simeq c\} \Vdash_E \underline{h(x_2) \not\simeq g(c)}} \text{ ASSIGN}}{\{x_1 \simeq c, x_3 \simeq c\} \Vdash_E \bot} \text{ R\_GEN}}{\{x_1 \simeq c, x_3 \simeq c\} \Vdash_E \bot} \text{ FAIL}$$

and $\mathcal{B}$:

$$\frac{\dfrac{\varnothing \Vdash_E \underline{x_1 \simeq a}, h(x_2) \not\simeq g(x_3), f(x_1) \simeq f(x_3)}{\{x_1 \simeq a\} \Vdash_E h(x_2) \not\simeq g(x_3), \underline{f(a) \simeq f(x_3)}} \text{ ASSIGN}}{} \text{ U\_COMP}$$

$$\frac{\dfrac{\{x_1 \simeq a\} \Vdash_E h(x_2) \not\simeq g(x_3), \underline{x_3 \simeq a}}{\{x_1 \simeq a, x_3 \simeq a\} \Vdash_E \underline{h(x_2) \not\simeq g(a)}} \text{ ASSIGN}}{\dfrac{\{x_1 \simeq a, x_3 \simeq a\} \Vdash_E \bot}{\{x_1 \simeq a, x_3 \simeq a\} \Vdash_E \bot} \text{ FAIL}} \text{ R\_GEN} \qquad \frac{\dfrac{\dfrac{\{x_1 \simeq a\} \Vdash_E h(x_2) \not\simeq g(x_3), \underline{x_3 \simeq b}}{\{x_1 \simeq a, x_3 \simeq b\} \Vdash_E \underline{h(x_2) \not\simeq g(b)}} \text{ ASSIGN}}{\dfrac{\{x_1 \simeq a, x_3 \simeq b\} \Vdash_E \underline{x_2 \simeq a}}{\{x_1 \simeq a, x_2 \simeq a, x_3 \simeq b\} \Vdash_E \top}} \text{ ASSIGN}}{\{x_1 \simeq a, x_2 \simeq a, x_3 \simeq b\} \Vdash_E \top} \text{ YIELD}}$$

A solution is produced by the rightmost branch of $\mathcal{B}$.

### 4.3   Correctness of CCFV

**Theorem 2 (Termination).** *All derivations in* CCFV *are finite.*

*Proof (Sketch).* The width of any split rule is always finite. It then suffices to show that the depth of the tree is bounded. For simplicity, but without any fundamental effect on the proof, let us assume that all rules but SPLIT apply on conjunctions. Let $d(C)$ be the sum of the depths of all occurrences of variables in the literals of the conjunction $C$. The ASSIGN rule decreases the number of variables of $C$. The FAIL and YIELD rules close a branch. All remaining rules from $E_\sigma \Vdash_E C$ to $E'_\sigma \Vdash_E C'_1 \vee \ldots \vee C'_n$ decrease $d$, i.e. $d(C) > d(C'_1), \ldots, d(C) > d(C'_n)$. At each node, $d(C)$ or the number of variables in $C$ are decreasing, except at the SPLIT steps. Since no branch can contain infinite sequences of SPLIT applications, the depth is always finite. □

**Lemma 1.** *Given a computed solution $E_\sigma$ for an $E$-ground (dis)unification problem $E \models L\sigma$, each $\sigma_g \in \text{SOLS}(E_\sigma)$ is an acyclic substitution such that $ran(\sigma_g) \subseteq \mathbf{T}(E \cup L)$ and $\sigma_g^\star$ is ground.*

*Proof (Sketch).* The proof can be found in Appendix D of [6]. □

**Lemma 2 (Rules capture entailment conditions).** *For each rule*

$$\frac{E_\sigma \Vdash_E C}{E'_\sigma \Vdash_E C'} \text{ R}$$

*and any ground substitution $\sigma$, $E \models (\{C\} \cup E_\sigma)\sigma$ iff $E \models (\{C'\} \cup E'_\sigma)\sigma$.*

*Proof (Sketch).* The proof can be found in Appendix D of [6]. □

**Theorem 3 (Soundness).** *Whenever a branch is closed with* YIELD, *every $\sigma_g \in \text{SOLS}(E_\sigma)$ is s.t. $E \models L\sigma_g^\star$.*

*Proof (Sketch).* Consider an arbitrary substitution $\sigma_g \in \text{SOLS}(E_\sigma)$ at the application of YIELD. Lemma 1 ensures that $\sigma_g^\star$ is ground. Thanks to the side condition of the YIELD rule and of the construction of $\sigma_g^\star$, $E \models (\{C\} \cup E_\sigma)\sigma_g^\star$ at the leaf. Then, thanks to Lemma 2, $E \models (\{C\} \cup E_\sigma)\sigma_g^\star$ also holds at the root, in which $C = L$ and $E_\sigma = \emptyset$. Thus $E \models L\sigma_g^\star$. □

**Theorem 4 (Completeness).** *Let $\sigma$ be a solution for an $E$-ground (dis)unification problem $E \models L\sigma$. Then there exists a derivation tree starting on $\varnothing \Vdash_E L$ with at least one branch closed with* YIELD *s.t. $\sigma_g \in \text{SOLS}(E_\sigma)$ and $E \models L\sigma_g^\star$.*

*Proof (Sketch).* By Theorem 1, there is an acyclic substitution $\sigma_g$ corresponding to $\sigma$ such that $ran(\sigma_g) \subseteq \mathbf{T}(E \cup L)$, $\sigma_g^\star$ is ground and $E \models L\sigma_g^\star$. Lemma 2 ensures that all rules in CCFV preserve the entailment conditions according to ground substitutions, therefore there is a branch in the derivation tree starting from $\varnothing \Vdash_E L$ whose leaf is $E_\sigma \Vdash_E \top$ and $\sigma_g \in \text{SOLS}(E_\sigma)$. □

**Corollary 1 (CCFV decides $E$-ground (dis)unification).** *Any derivation strategy based on the* CCFV *calculus is a decision procedure to find all solutions $\sigma$ for the $E$-ground (dis)unification problem $E \models L\sigma$.*

# 5    Relation to Instantiation Techniques

Here we discuss how different instantiation techniques for evaluating a candidate model $E \cup \mathcal{Q}$ can be related with $E$-ground (dis)unification and thus integrated with CCFV.

## 5.1    Trigger Based Instantiation

The most common instantiation technique in SMT solving is a heuristic one: its search is based solely on $E$-matching of selected triggers [12, 17, 26], without further semantic criteria. A *trigger* $T$ for a quantified formula $\forall \mathbf{x}.\psi \in \mathcal{Q}$ is a set of terms $f_1(\mathbf{s}_1), \ldots, f_n(\mathbf{s}_n) \in \mathbf{T}(\psi)$ s.t. $\{\mathbf{x}\} \subseteq \mathrm{FV}(f_1(\mathbf{s}_1)) \cup \cdots \cup \mathrm{FV}(f_n(\mathbf{s}_n))$. Instantiations are determined by $E$-matching all terms in $T$ with terms in $\mathbf{T}(E)$, such that resulting substitutions allow instantiating $\forall \mathbf{x}.\psi$ into ground formulas. Computing such substitutions amounts to solving the $E$-ground (dis)unification problem

$$E \models (f_1(\mathbf{s}_1) \simeq y_1 \wedge \cdots \wedge f_n(\mathbf{s}_n) \simeq y_n)\, \sigma$$

with the further restriction that $\sigma$ is acyclic, $ran(\sigma) \subseteq \mathbf{T}(E \cup L)$ and $\sigma$ is ground. This forces each $y_i$ to be grounded into a term in $\mathbf{T}(E)$, thus enumerating all possibilities for $E$-matching $f_i(\mathbf{s}_i)$.[2] The desired instantiations are obtained by restricting the found solutions to $\mathbf{x}$.

*Example 4.* Consider the sets $E = \{f(a) \simeq g(b),\ h(a) \simeq b, f(a) \simeq f(c)\}$ and $\mathcal{Q} = \{\forall x.\ f(x) \not\simeq g(h(x))\}$. Triggers from $\mathcal{Q}$ are $T_1 = \{f(x)\}$, $T_2 = \{h(x)\}$, $T_3 = \{f(x), g(h(x))\}$ and so on. The instantiations from those triggers are derived from the solutions yielded by CCFV for the respective problems:

- $E \models (f(x) \simeq y)\sigma$, solved by substitutions $\sigma_1 = \{y \mapsto f(a), x \mapsto a\}$ and $\sigma_2 = \{y \mapsto f(c), x \mapsto c\}$
- $E \models (h(x) \simeq y)\sigma$, solved by $\sigma = \{y \mapsto h(a), x \mapsto a\}$
- $E \models (f(x) \simeq y_1 \wedge g(h(x)) \simeq y_2)\sigma$, by $\sigma = \{y_1 \mapsto f(a), y_2 \mapsto g(b), x \mapsto a\}$

**Discarding Entailed Instances.** Trigger-based instantiation may produce instances which are already entailed by the ground model. Such instances most probably will not contribute to the solving, so they should be discarded. Checking this, however, is not straightforward with pre-processing techniques. CCFV, on the other hand, allows it by simply checking, given an instantiation $\sigma$ for a quantified formula $\forall \mathbf{x}.\psi$, whether there is a literal $\ell \in \psi$ s.t. $E \cup E_\sigma \models \ell$, with $E_\sigma = \{x \simeq x\sigma \mid x \in dom(\sigma)\}$.

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[2] For CCFV to generate such solutions it is sufficient to add the side condition to ASSIGN that $s$ is a variable or a ground term and to remove the side condition of U_VAR. This will lead to the application of U_VAR in each $f_i(\mathbf{s}_1) \simeq y_i$.

## 5.2  Conflict Based Instantiation

A goal-oriented instantiation technique was introduced by Reynolds et al. [24] to provide fewer and more meaningful instances. Quantified formulas are evaluated, independently, in search for *conflicting instances*: for each quantified formula $\forall \mathbf{x}.\psi \in \mathcal{Q}$, only instances $\psi\sigma$ for which $E \cup \psi\sigma$ is unsatisfiable are derived. Such instances force the derivation of a new candidate model $E \cup \mathcal{Q}$ for the formula. Finding a conflicting instance amounts to solving the $E$-ground (dis)unification problem

$$E \models \neg\psi\sigma, \text{ for some } \forall \mathbf{x}.\psi \in \mathcal{Q}$$

since $\neg\psi$ is a conjunction of equality literals. Differently from the algorithm shown in [24], CCFV finds all conflicting instantiations for a given quantified formula.

*Example 5.* Let $E$ and $\mathcal{Q}$ be as in Example 4. Applying CCFV in the problem

$$E \models (f(x) \simeq g(h(x)))\,\sigma$$

leads to the sole conflicting instantiation $\sigma = \{x \mapsto a\}$.

**Propagating Equalities.** As discussed in [24], even when the search for conflicting instances fails it is still possible to "propagate" equalities. Given some $\neg\psi = \ell_1 \wedge \cdots \wedge \ell_n$, let $\sigma$ be a ground substitution s.t. $E \models \ell_1\sigma \wedge \cdots \wedge \ell_{k-1}\sigma$ and all remaining literals $\ell_k\sigma, \ldots, \ell_n\sigma$ not entailed are ground disequalities with $(\mathbf{T}(\ell_k) \cup \cdots \cup \mathbf{T}(\ell_n)) \subseteq \mathbf{T}(E)$. The instantiation $\forall \mathbf{x}.\psi \to \psi\sigma$ introduces a disjunction of equalities constraining $\mathbf{T}(E)$. CCFV can generate such propagating substitutions if the side conditions of FAIL and YIELD are relaxed w.r.t. ground disequalities whose terms occur in $\mathbf{T}(E)$ and originally had variables: the former is not applied based on them and the latter is if all other literals are entailed.

*Example 6.* Consider $E = \{f(a) \simeq t, t' \simeq g(a)\}$ and $\forall x.\ f(x) \not\simeq t \vee f(x) \simeq g(x)$. When applying CCFV in the problem

$$E \models (f(x) \simeq t \wedge f(x) \not\simeq g(x))\,\sigma$$

to entail the first literal a candidate solution $E_\sigma = \{x \simeq a\}$ is produced. The second literal would then be normalized to $f(a) \not\simeq g(a)$, which would lead to the application of FAIL, since it is not entailed by $E$. However, as it is a disequality whose terms are in $\mathbf{T}(E)$ and originally had variables, the rule applied is YIELD instead. The resulting substitution $\sigma = \{x \mapsto a\}$ leads to propagating the equality $f(a) \simeq g(a)$, which merges two classes previously different in $E^{\text{CC}}$.

## 5.3  Model Based Instantiation (MBQI)

A complete instantiation technique was introduced by Ge and de Moura [19]. The set $E$ is extended into a total model, each quantified formula is evaluated in

this total model, and conflicting instances are generated. The successive rounds of instantiation either lead to unsatisfiability or, when no conflicting instance is generated, to satisfiability with a concrete model. Here we follow the model construction guidelines by Reynolds et al. [25].

A distinguished term $e^\tau$ is associated to each sort $\tau \in \mathcal{S}$. For each $f \in \mathcal{F}$ with sort $\langle \tau_1, \ldots, \tau_n, \tau \rangle$ a *default value* $\xi_f$ is defined such that

$$\xi_f = \begin{cases} f(t_1, \ldots, t_n) \in \mathbf{T}(E) & \text{if } [t_1] = [e^{\tau_1}], \ldots, [t_n] = [e^{\tau_n}] \\ \text{some } t \in \mathbf{T}(E) & \text{otherwise} \end{cases}$$

The extension $E_{\text{TOT}}$ is built s.t. all fresh ground terms which might be considered when evaluating $\mathcal{Q}$ are in its congruence closure, according to the respective default values; and all terms in $\mathbf{T}(E)$ not asserted equal are explicitly asserted disequal, i.e.

$$E_{\text{TOT}} = E \cup \bigcup_{t_1, t_2 \in \mathbf{T}(E)} \{t_1 \not\simeq t_2 \mid E \not\models t_1 \simeq t_2\}$$
$$\bigcup_{\forall \mathbf{x}. \psi \in \mathcal{Q}, \mathbf{t} \in \mathbf{T}(E)} \left\{ f(\mathbf{s})\sigma \simeq \xi_f \;\middle|\; \begin{array}{l} \sigma = \{\mathbf{x} \mapsto \mathbf{t}\}, f(\mathbf{s}) \in \mathbf{T}(\psi) \text{ and} \\ f(\mathbf{s})\sigma \text{ is not in the CC of } E. \end{array} \right\}$$

As before, finding conflicting instances amounts to solving the $E$-ground (dis)unification problem

$$E_{\text{TOT}} \models \neg\psi\sigma, \text{ for some } \forall \mathbf{x}. \psi \in \mathcal{Q}$$

*Example 7.* Let $E = \{f(a) \simeq g(b), h(a) \simeq b\}$, $\mathcal{Q} = \{\forall x. f(x) \not\simeq g(x), \forall xy. \psi\}$ and $e = a$, with all terms having the same sort. The computed default values of the function symbols are $\xi_f = f(a), \xi_g = a, \xi_h = h(a)$. For simplicity, the extension $E_{\text{TOT}}$ is shown explicitly only for $\forall x. f(x) \not\simeq g(x)$,

$$E_{\text{TOT}} = E \cup \{a \not\simeq b, a \not\simeq f(a), b \not\simeq f(a)\}$$
$$\cup \{f(b) \simeq f(a), f(f(a)) \simeq f(a), g(a) \simeq a, g(f(a)) \simeq a\} \cup \{\ldots\}$$

Applying CCFV in

$$\{\ldots, f(a) \simeq g(b), f(b) \simeq f(a), \ldots\} \models f(x) \simeq g(x)\sigma$$

leads to a conflicting instance with $\sigma = \{x \mapsto b\}$. Notice that it is not necessary to explicitly build $E_{\text{TOT}}$, which can be quite large. Terms can be defined lazily as they are required by CCFV for building potential solutions.

## 6   Implementation and Experiments

CCFV has been implemented in the veriT [11] and CVC4 [7] solvers. As is common in SMT solvers, they make use of an $E$-graph to represent the set of signature classes $E^{\text{CC}}$ and efficiently check ground entailment.[3] Indexing techniques for fast

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[3] Currently the ground congruence closure procedures are not closed under entailment w.r.t. disequalities. E.g. $g(f(a), h(b)) \not\simeq g(f(b), h(a)) \in E$ does not lead to the addition of $a \not\simeq b$ to the data structure. A complete implementation of CCFV requires the ground congruence closure to entail all entailed disequalities.

retrieval of candidates are paramount for a practical procedure, so $E^{\mathrm{cc}}$ is indexed by top symbols. Each function symbol points to all their related signatures. They are kept sorted by congruence classes to allow binary search when retrieving all signatures with a given top symbol congruent to a given term. To quickly discard classes without signatures with a given top symbol, bit masks are associated to congruence classes: each symbol is assigned an arbitrary bit, and the mask for the class is the set of all bits of the top symbols. Another important optimization is to minimize $E$, since the candidate model $E \cup \mathcal{Q}$ produced by the SAT solver and guiding the instantiation is generally not minimal. A minimal partial model (a *prime implicant*) for the CNF is computed in linear time [16], and this model is further reduced to circumvent the effect of the CNF transformation, using a process similar to the one described by de Moura and Bjørner [12] for *relevancy*.

During rule application, matching a term $f(\mathbf{u})$ with a ground term $f(\mathbf{t})$ fails unless all the ground arguments are pairwise congruent. Thus after an assignment, if an argument of a term $f(\mathbf{u})$ in a branching constraint becomes ground, it can be checked whether there is a ground term $f(\mathbf{t}) \in \mathbf{T}(E)$ s.t., for every ground argument $u_i$, $E \models u_i \simeq t_i$. If no such term exists and $f(\mathbf{u})$ is not in a literal amenable for U\_COMP, the branch can be *eagerly discarded*. For this technique, a dedicated index for each function symbol $f$ maps tuples of pairs, with a ground term and a position, $\langle (t_1, i_1), \ldots, (t_k, i_k) \rangle$ to all signatures $f(t'_1, \ldots, t'_n)$ in $E^{\mathrm{cc}}$ s.t. $E \models t_1 \simeq t'_{i_1}, \ldots, E \models t_k \simeq t'_{i_k}$, i.e. all signatures whose arguments, in the respective positions, are congruent with the given ground terms.

**Experiments.** Here we evaluate the impact of optimizations and instantiation techniques based on CCFV over previous versions and compare them against the state-of-the-art instantiation based solver Z3 [14]. Different configurations are identified in this section according to which techniques and algorithms they have activated:

**t**: trigger instantiation through CCFV;
**c**: conflict based instantiation through CCFV;
**e**: optimization for eagerly discarding branches with unmatchable applications;
**d**: discards already entailed trigger based instances (as in Sect. 5.1)

The configuration **verit** refers to the previous version of veriT, which only offered support for quantified formulas through naïve trigger instantiation, without further optimizations. The configuration **cvc** refers to version 1.5 of CVC4, which applies **t** and **c** by default, as well as propagation of equalities. Both implementations of CCFV include efficient term indexing and apply a simple selection heuristic, checking ground and reflexive literals first but otherwise considering the conjunction of constraints as a queue. The evaluation was made on the UF, UFLIA, UFLRA and UFIDL categories of SMT-LIB [9], with $10\,495$ benchmarks annotated as *unsatisfiable*, mostly stemming for verification and ITP platforms. The categories with bit vectors and non-linear arithmetic are currently not supported by veriT and in those in which uninterpreted functions are not predominant the techniques shown here are not as effective. Our experiments were conducted using machines with 2 CPUs Intel Xeon E5-2630 v3,
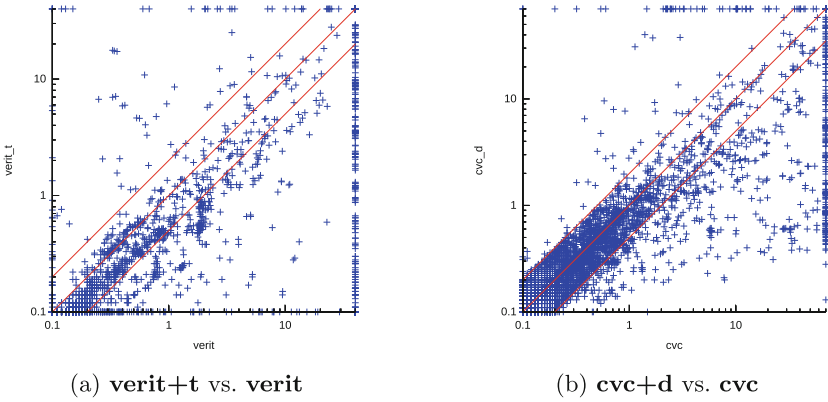
(a) **verit+t** vs. **verit**

(b) **cvc+d** vs. **cvc**

**Fig. 1.** Improvements in veriT and CVC4

8 cores/CPU, 126 GB RAM, 2x558 GB HDD. The timeout was set for 30 s, since our goal is evaluating SMT solvers as back-ends of verification and ITP platforms, which require fast answers.

Figure 1 exhibits an important impact of CCFV and the techniques and optimizations built on top of it. **verit+t** performs much better than **verit**, solely due to CCFV. **cvc+d** improves significantly over **cvc**, exhibiting the advantage of techniques based on the entailment checking features of CCFV. The comparison between the different configurations of veriT and CVC4 with the SMT solver Z3 (version 4.4.2) is summarized in Table 2, excluding categories whose problems are trivially solved by all systems, which leaves 8 701 problems for consideration. **verit+tc** shows further improvements, solving approximately the same number of problems as Z3, although mostly because of the better performance on the *sledgehammer* benchmarks, containing less theory symbols. It also

**Table 2.** Instantiation based SMT solvers on SMT-LIB benchmarks

| Logic | Class | Z3 | cvc+d | cvc+e | cvc | verit+tc | verit+t | verit |
|-------|-------|-----|-------|-------|------|----------|---------|-------|
| UF | grasshopper | 418 | 411 | 420 | 415 | **430** | 418 | 413 |
| | sledgehammer | 1249 | 1438 | **1456** | 1428 | 1265 | 1134 | 1066 |
| UFIDL | all | **62** | **62** | **62** | **62** | 58 | 58 | 58 |
| UFLIA | boogie | **852** | 844 | 834 | 801 | 705 | 660 | 661 |
| | sexpr | **26** | 12 | 11 | 11 | 7 | 5 | 5 |
| | grasshopper | 341 | 322 | 326 | 319 | **357** | 340 | 335 |
| | sledgehammer | 1581 | 1944 | **1953** | 1929 | 1783 | 1620 | 1569 |
| | simplify | **831** | 766 | 706 | 705 | 803 | 735 | 690 |
| | simplify2 | **2337** | 2330 | 2292 | 2286 | 2304 | 2291 | 2177 |
| | Total | 7697 | **8129** | 8060 | 7956 | 7712 | 7261 | 6916 |

performs best in the *grasshopper* families, stemming from the heap verification tool GRASShopper [23]. Considering the overall performance, both **cvc+d** and **cvc+e** solve significantly more problems than **cvc**, specially in benchmarks from verification platforms, approaching the performance of Z3 in these families. Both these techniques, as well as the propagation of equalities, are fairly important points in the performance of CVC4, so their implementation is a clear direction for improvements in veriT.

## 7  Conclusion and Future Work

We have introduced CCFV, a decision procedure for $E$-ground (dis)unification, and shown how the main instantiation techniques of SMT solving may be based on it. Our experimental evaluation shows that CCFV leads to significant improvements in the solvers CVC4 and veriT, making the former surpass the state-of-the-art in instantiation based SMT solving and the latter competitive in several benchmark libraries. The calculus presented is very general, allowing for different strategies and optimizations, as discussed in previous sections.

A direction for improvement is to use *lemma learning* in CCFV, in a similar manner as SAT solvers do. When a branch fails to produce a solution and is discarded, analyzing the literals which led to the conflict can allow *backjump* rather than simple backtracking, thus further reducing the solution search space. The *Complementary Congruence Closure* introduced by Backeman and Rümmer [4] could be extended to perform such an analysis.

Like other main instantiation techniques in SMT, the framework here focuses on the theory of equality only. Extensions to first-order theories such as arithmetic are left for future work. The implementation of MBQI based on CCFV, whose theoretical suitability we outlined, is left for future work as well. Another possible extension of CCFV is to handle rigid $E$-unification, so it could be applied in techniques such as BREU [5]. This amounts to have non-ground equalities in $E$, so it is not trivial. It would, however, allow integrating an efficient goal-oriented procedure into $E$-unification based calculi.

## References

1. Baader, F., Nipkow, T.: Term Rewriting and All That. Cambridge University Press, New York (1998)
2. Baader, F., Snyder, W.: Unification theory. In: Robinson, J.A., Voronkov, A., (eds) Handbook of Automated Reasoning, pp. 445–532. Elsevier and MIT Press (2001)
3. Bachmair, L., Ganzinger, H.: Rewrite-based equational theorem proving with selection and simplification. J. Logic Comput. **4**(3), 217–247 (1994)

4. Backeman, P., Rümmer, P.: Efficient algorithms for bounded rigid $E$-unification. In: Nivelle, H. (ed.) TABLEAUX 2015. LNCS (LNAI), vol. 9323, pp. 70–85. Springer, Heidelberg (2015). doi:10.1007/978-3-319-24312-2_6

5. Backeman, P., Rümmer, P.: Theorem proving with bounded rigid $E$-unification. In: Felty, A.P., Middeldorp, A. (eds.) CADE 2015. LNCS (LNAI), vol. 9195, pp. 572–587. Springer, Heidelberg (2015). doi:10.1007/978-3-319-21401-6_39

6. Barbosa, H., Fontaine, P., Reynolds, A.: Congruence closure with free variables. Technical report, Inria (2016). https://hal.inria.fr/hal-01442691

7. Barrett, C., Conway, C.L., Deters, M., Hadarean, L., Jovanović, D., King, T., Reynolds, A., Tinelli, C.: CVC4. In: Gopalakrishnan, G., Qadeer, S. (eds.) CAV 2011. LNCS, vol. 6806, pp. 171–177. Springer, Heidelberg (2011). doi:10.1007/978-3-642-22110-1_14

8. Barrett, C., Sebastiani, R., Seshia, S., Tinelli, C.: Satisfiability modulo theories. In: Biere, A., Heule, M.J.H., van Maaren, H., Walsh, T. (eds.) Handbook of Satisfiability. Frontiers in Artificial Intelligence and Applications, vol. 185, pp. 825–885. IOS Press, Amsterdam (2009)

9. Barrett, C., Stump, A., Tinelli, C.: The SML-LIB standard: version 2.0. In: Gupta, A., Kroening, D. (eds) International Workshop on Satisfiability Modulo Theories (SMT) (2010)

10. Beckert, B.: Ridig E-unification. In: Bibel, W., Schimidt, P.H. (eds.) Automated Deduction: A Basis for Applications. Foundations: Calculi and Methods, vol. 1. Kluwer Academic Publishers, Dordrecht (1998)

11. Bouton, T., de Oliveira, D.C.B., Fontaine, P.: veriT: an open, trustable and efficient SMT-solver. In: Schmidt, R.A. (ed.) CADE 2009. LNCS (LNAI), vol. 5663, pp. 151–156. Springer, Heidelberg (2009). doi:10.1007/978-3-642-02959-2_12

12. de Moura, L., Bjørner, N.: Efficient E-matching for SMT solvers. In: Pfenning, F. (ed.) CADE 2007. LNCS (LNAI), vol. 4603, pp. 183–198. Springer, Heidelberg (2007). doi:10.1007/978-3-540-73595-3_13

13. de Moura, L., Bjørner, N.: Engineering DPLL(T) + saturation. In: Armando, A., Baumgartner, P., Dowek, G. (eds.) IJCAR 2008. LNCS (LNAI), vol. 5195, pp. 475–490. Springer, Heidelberg (2008). doi:10.1007/978-3-540-71070-7_40

14. de Moura, L., Bjørner, N.: Z3: an efficient SMT solver. In: Ramakrishnan, C.R., Rehof, J. (eds.) TACAS 2008. LNCS, vol. 4963, pp. 337–340. Springer, Heidelberg (2008). doi:10.1007/978-3-540-78800-3_24

15. Degtyarev, A., Voronkov, A.: Equality reasoning in sequent-based calculi. In: Robinson, J.A., Voronkov, A. (eds.) Handbook of Automated Reasoning, pp. 611–706. Elsevier, Amsterdam (2001)

16. Déharbe, D., Fontaine, P., Le Berre, D., Mazure, B.: Computing prime implicants. In: Formal Methods in Computer-Aided Design (FMCAD), pp. 46–52. IEEE (2013)

17. Detlefs, D., Nelson, G., Saxe, J.B.: Simplify: a theorem prover for program checking. J. ACM **52**(3), 365–473 (2005)

18. Fitting, M.: First-Order Logic and Automated Theorem Proving. Springer, New York (1990)

19. Ge, Y., de Moura, L.: Complete instantiation for quantified formulas in satisfiabiliby modulo theories. In: Bouajjani, A., Maler, O. (eds.) CAV 2009. LNCS, vol. 5643, pp. 306–320. Springer, Heidelberg (2009). doi:10.1007/978-3-642-02658-4_25

20. Goubault, J.: A rule-based algorithm for rigid E-unification. In: Gottlob, G., Leitsch, A., Mundici, D. (eds.) KGC 1993. LNCS, vol. 713, pp. 202–210. Springer, Heidelberg (1993). doi:10.1007/BFb0022569

21. Nelson, G., Oppen, D.C.: Fast decision procedures based on congruence closure. J. ACM **27**(2), 356–364 (1980)

22. Nieuwenhuis, R., Oliveras, A.: Fast congruence closure, extensions. Inf. Comput. **205**(4), 557–580 (2007). Special Issue: 16th International Conference on Rewriting Techniques and Applications

23. Piskac, R., Wies, T., Zufferey, D.: GRASShopper - complete heap verification with mixed specifications. In: Ábrahám, E., Havelund, K. (eds.) TACAS 2014. LNCS, vol. 8413, pp. 124–139. Springer, Heidelberg (2014). doi:10.1007/978-3-642-54862-8_9

24. Reynolds, A., Tinelli, C., de Moura, L.: Finding conflicting instances of quantified formulas in SMT. In: Formal Methods in Computer-Aided Design (FMCAD), pp. 195–202. FMCAD Inc (2014)

25. Reynolds, A., Tinelli, C., Goel, A., Krstić, S., Deters, M., Barrett, C.: Quantifier instantiation techniques for finite model finding in SMT. In: Bonacina, M.P. (ed.) CADE 2013. LNCS (LNAI), vol. 7898, pp. 377–391. Springer, Heidelberg (2013). doi:10.1007/978-3-642-38574-2_26

26. Rümmer, P.: E-matching with free variables. In: Bjørner, N., Voronkov, A. (eds.) LPAR 2012. LNCS, vol. 7180, pp. 359–374. Springer, Heidelberg (2012). doi:10.1007/978-3-642-28717-6_28

27. Tiwari, A., Bachmair, L., Ruess, H.: Rigid $E$-unification revisited. In: McAllester, D. (ed.) CADE 2000. LNCS (LNAI), vol. 1831, pp. 220–234. Springer, Heidelberg (2000). doi:10.1007/10721959_17